

Fig. 1

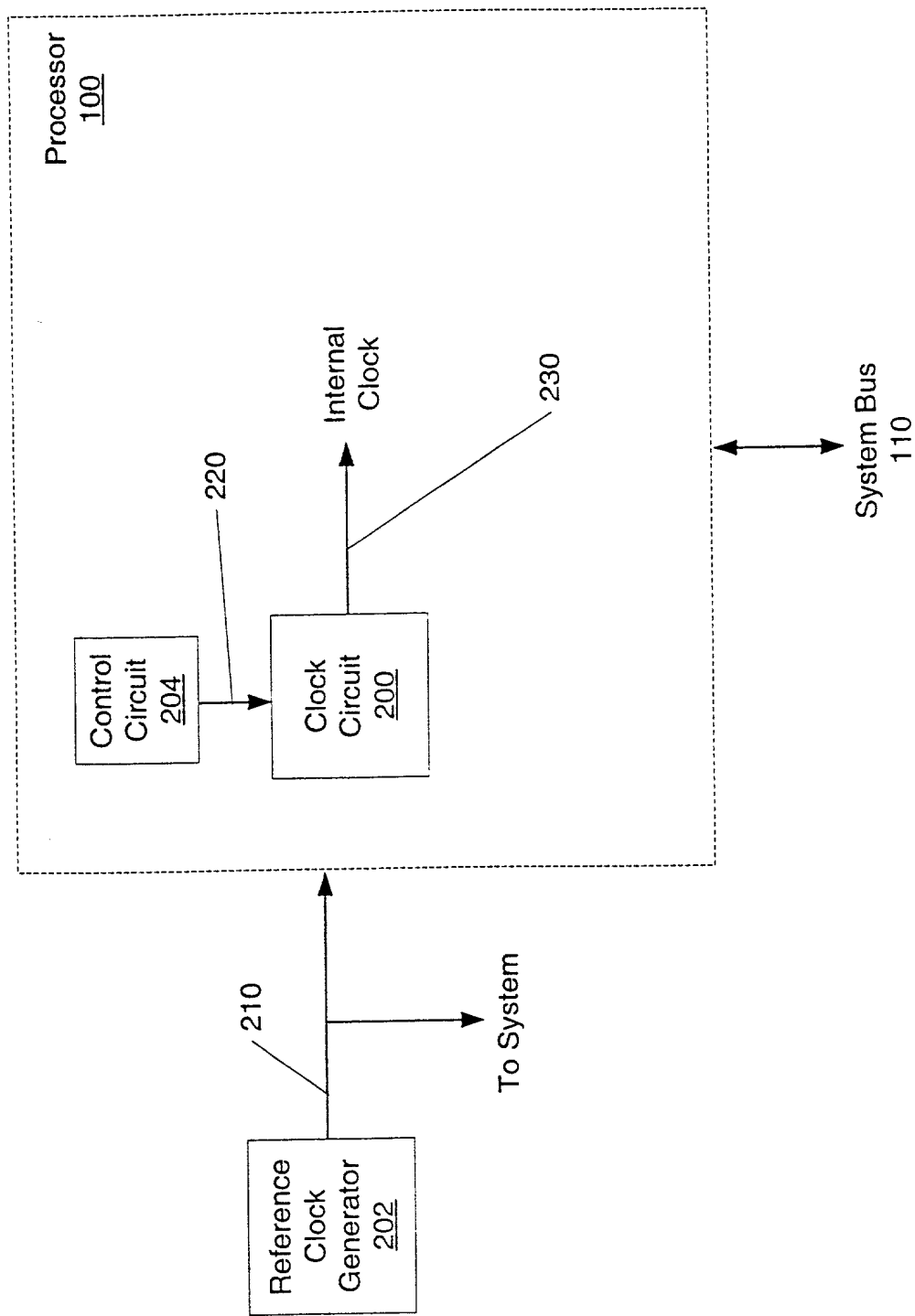


Fig. 2

300

Reference Frequency (MHz) <u>301</u>	Divisor <u>302</u>	Frequency (MHz) <u>303</u>	Dropped Pulses (out of 8) <u>304</u>	Effective Divisor <u>305</u>	Effective Frequency (MHz) <u>306</u>
1000	1	1000	0	1	1000
1000	1	1000	1	1.14	875
1000	1	1000	2	1.33	750
1000	1	1000	3	1.6	675
1000	2	500	0	2	500
1000	2	500	2	2.67	375
1000	4	250	0	4	250
1000	8	125	0	8	125

Fig. 3

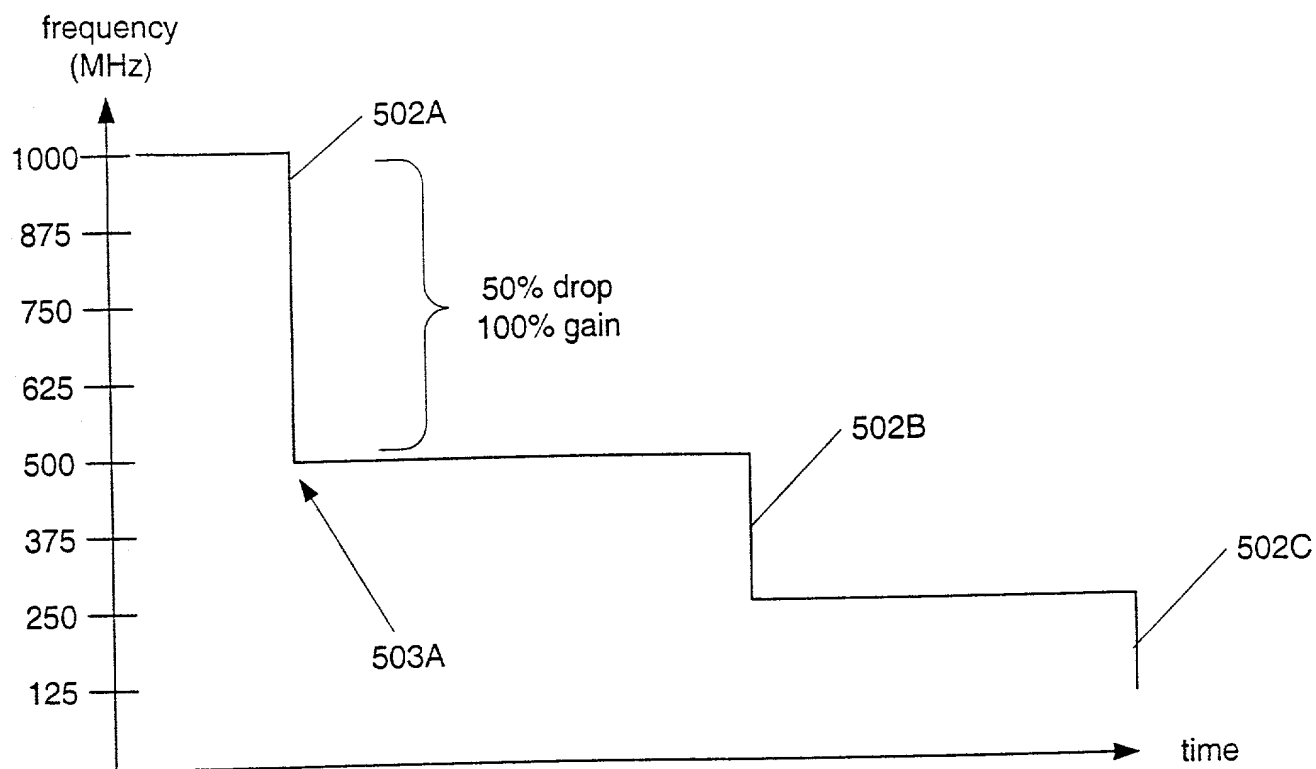


Fig. 4A

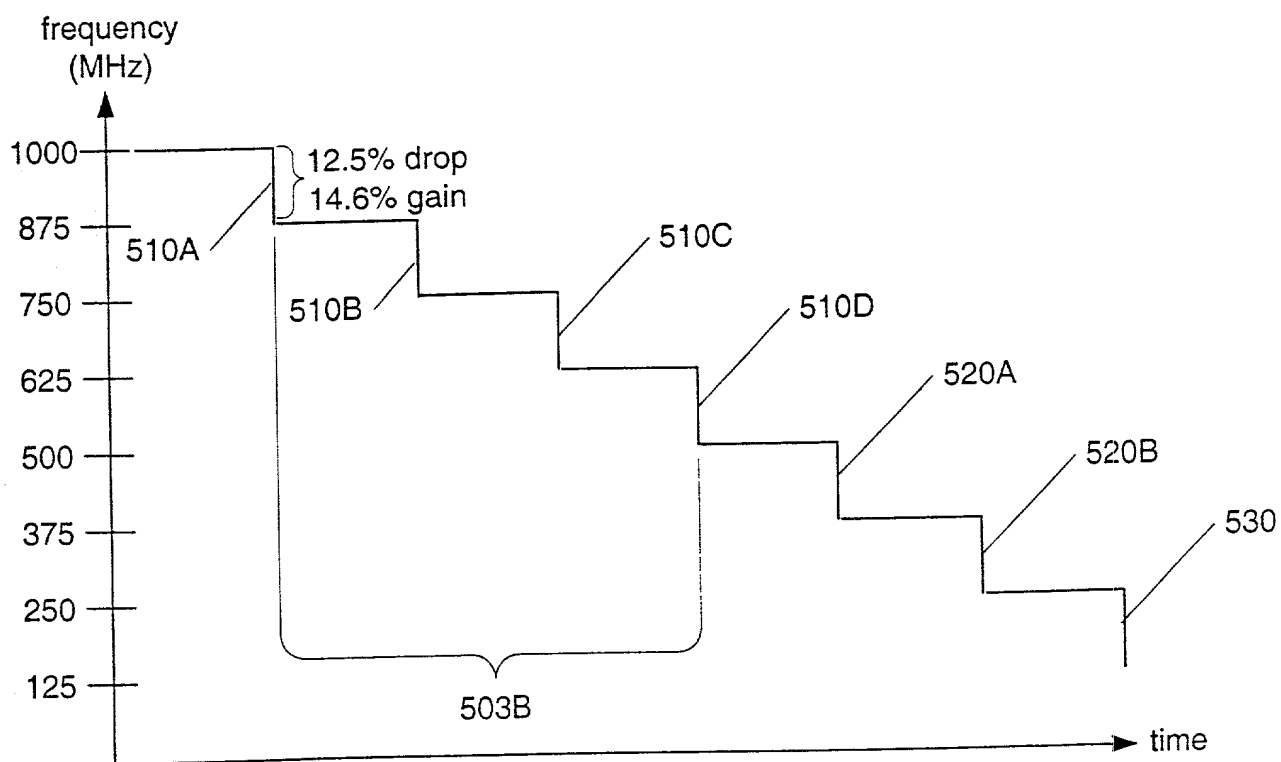


Fig. 4B

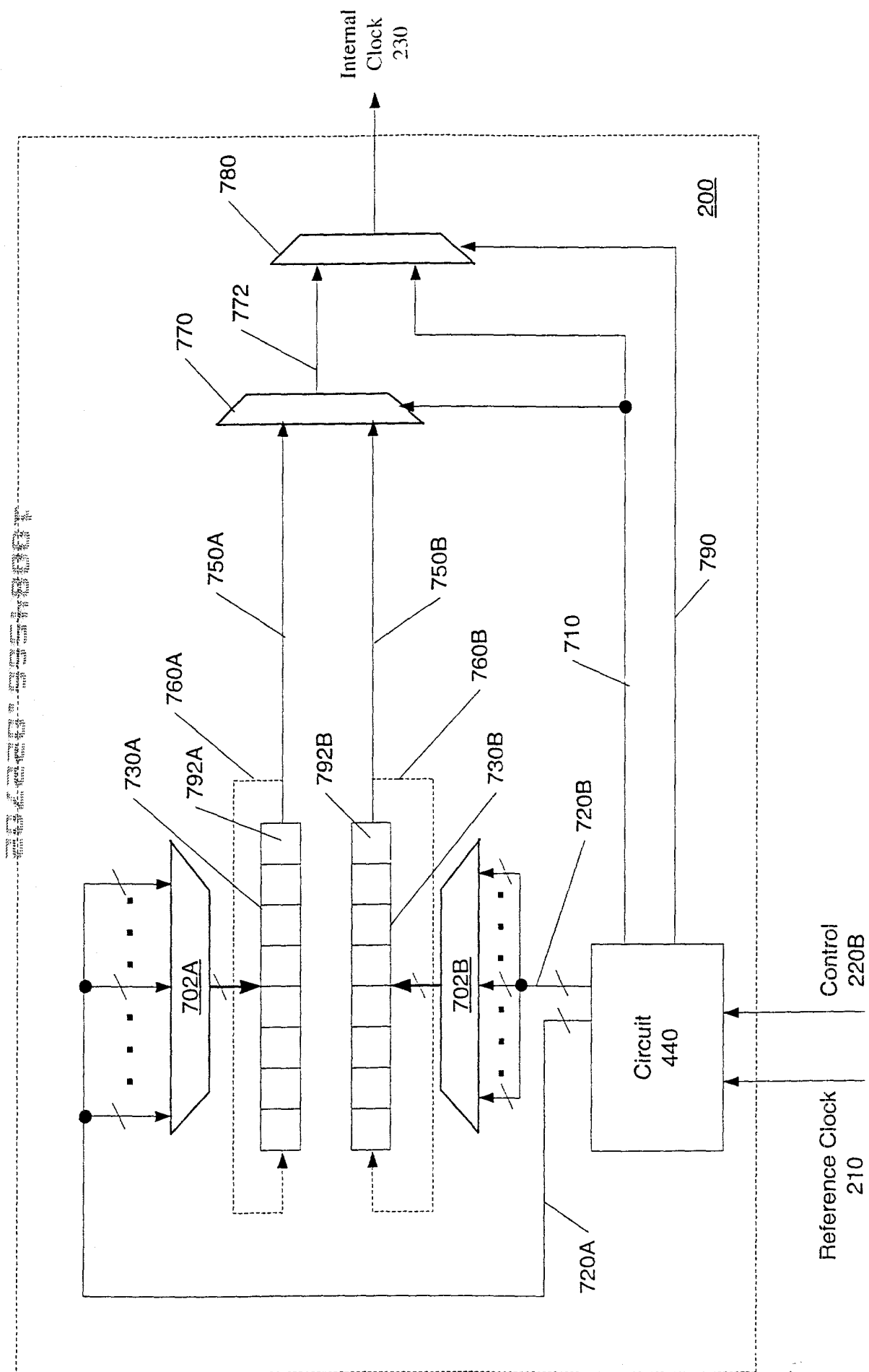


Fig. 5

FIG. 6A is a block diagram of a system 600. The system 600 includes a processor 610, a memory 620, and a display 630. The processor 610 is connected to the memory 620 and the display 630. The memory 620 stores data 640. The display 630 displays the data 640. The system 600 is configured to process the data 640 and display the results on the display 630.

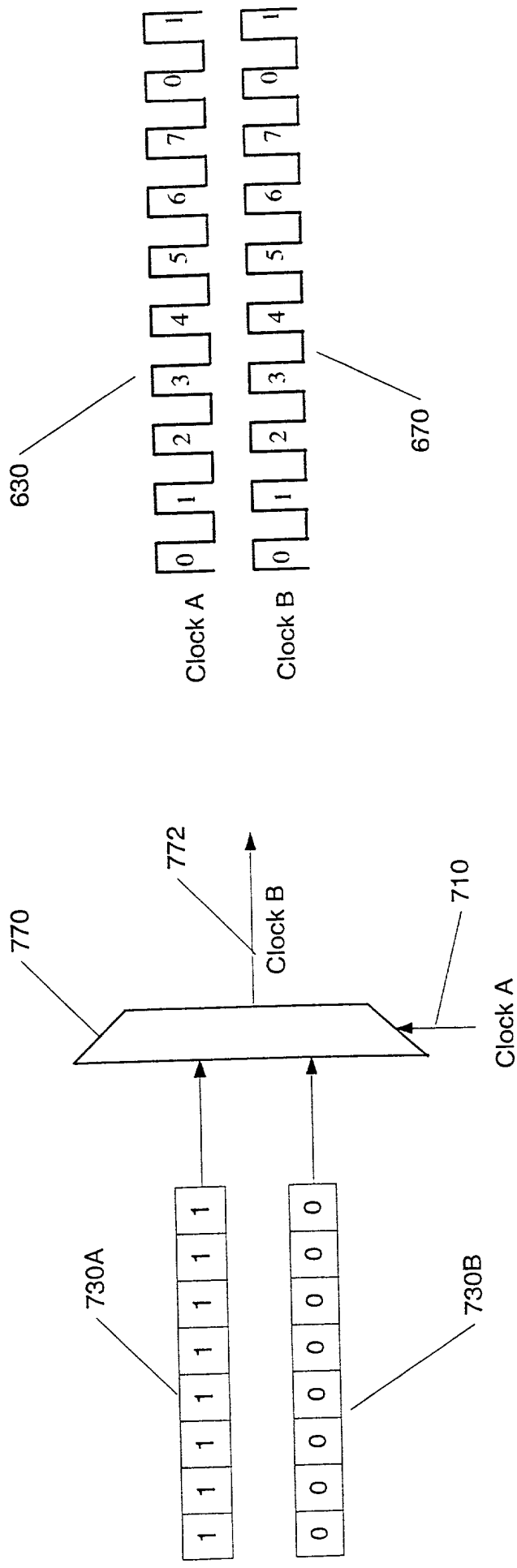


Fig. 6A

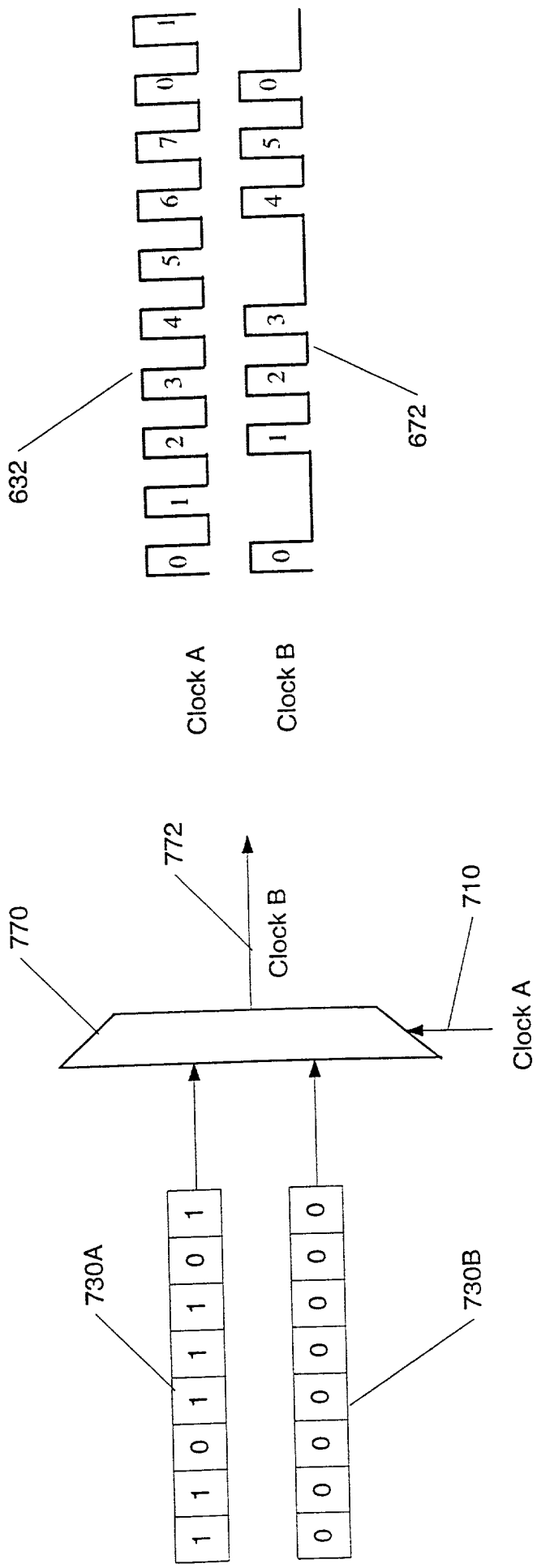


Fig. 6B

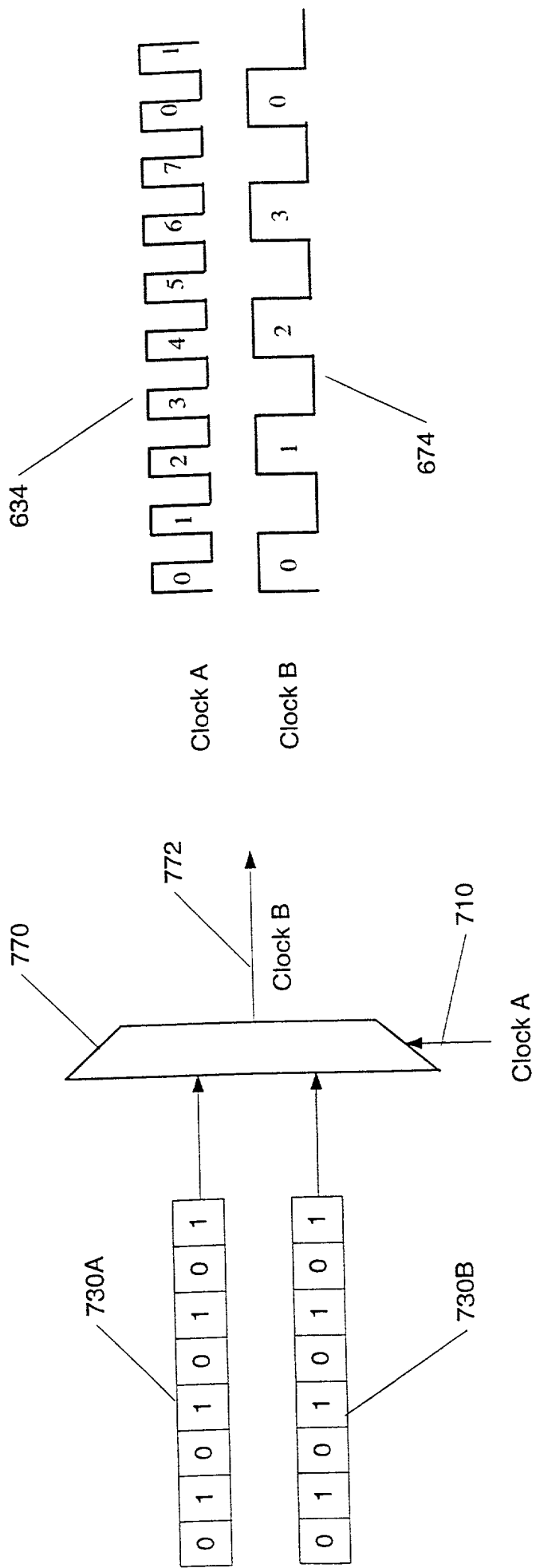


Fig. 6C

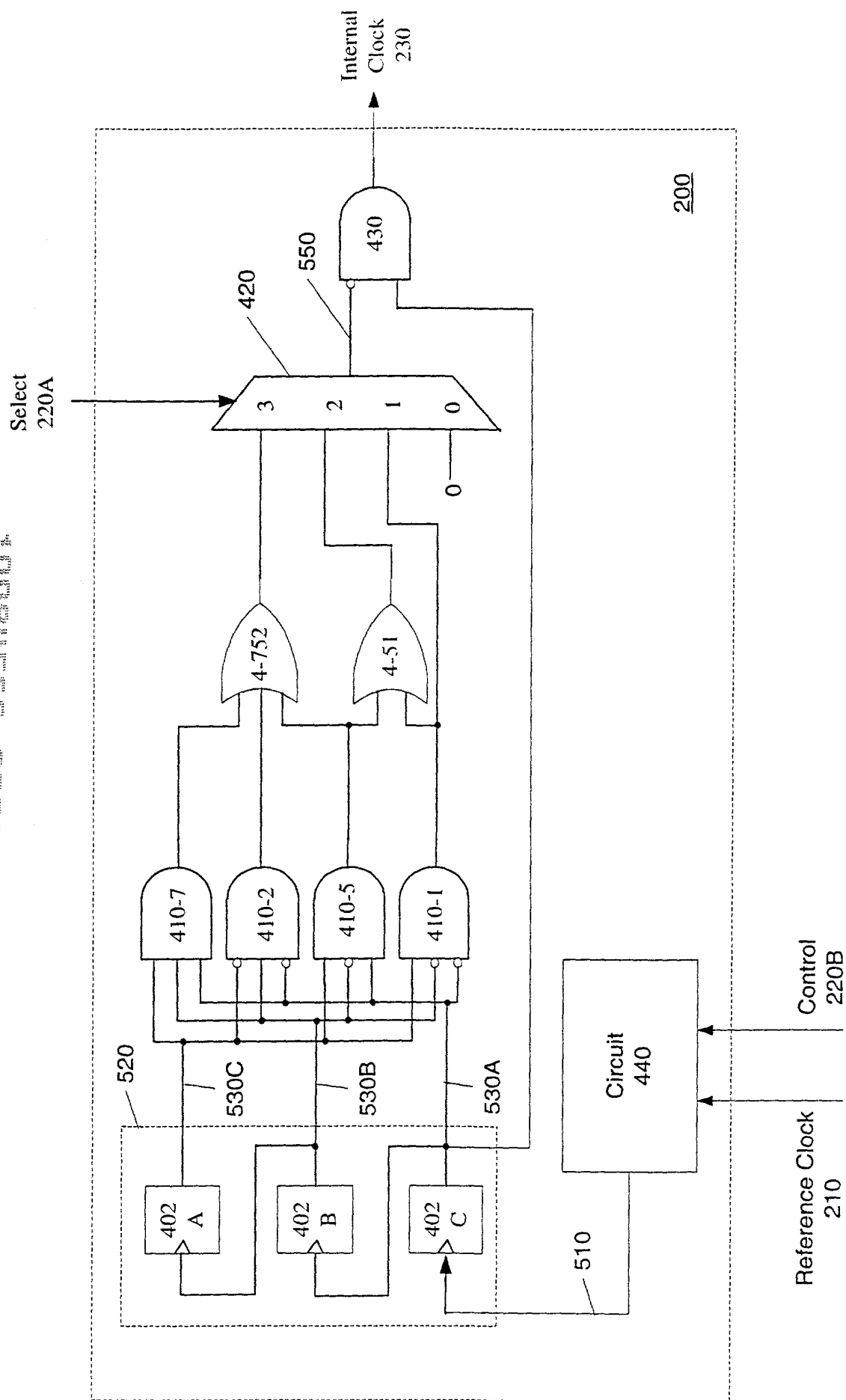


Fig. 7

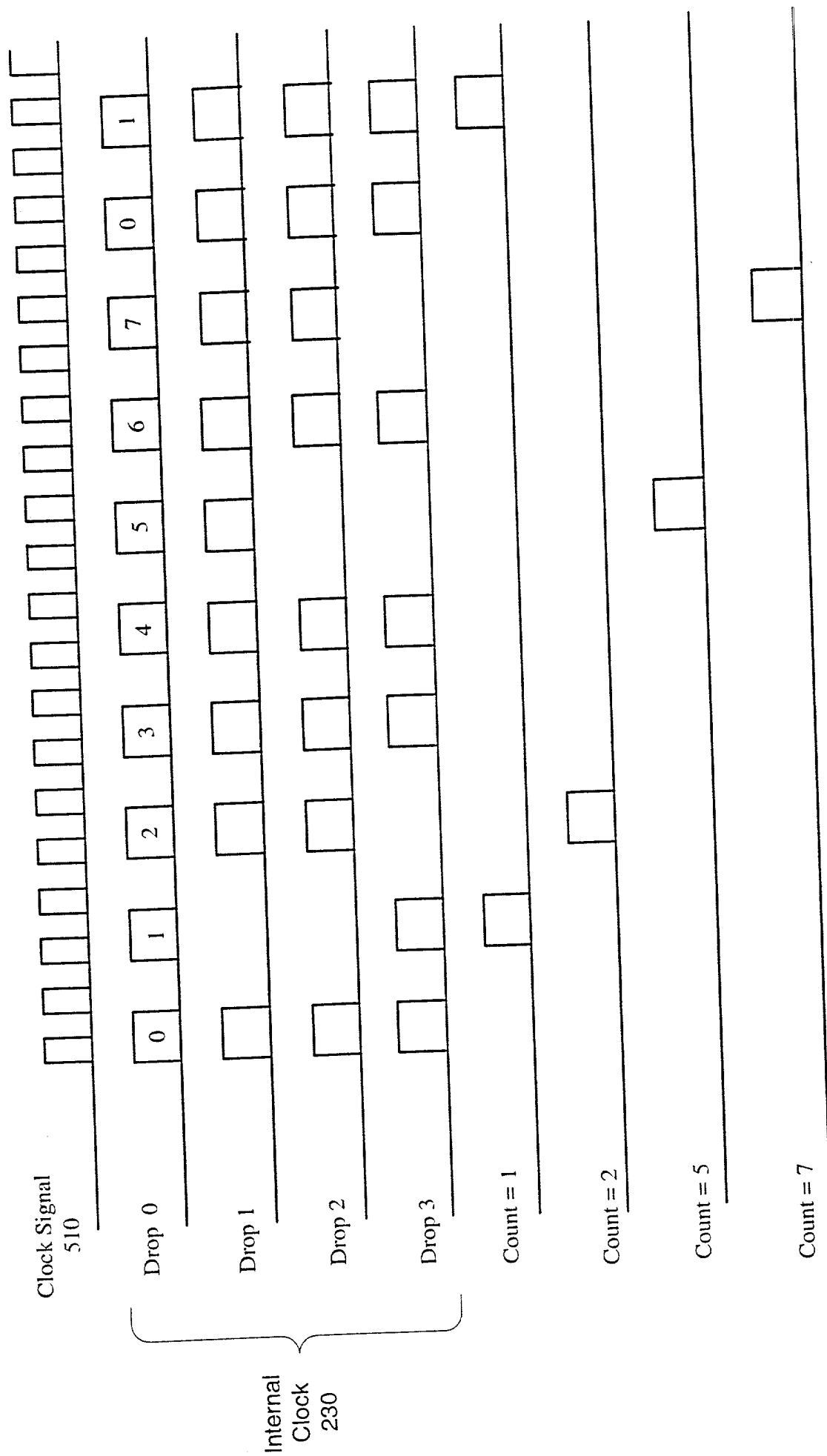


Fig. 8